

### **General Purpose Transistors**

**NPN Silicon** 

BC817-16L, SBC817-16L, BC817-25L, SBC817-25L, BC817-40L, SBC817-40L

# COLLECTOR 3 BASE 2 EMITTER



SOT-23 CASE 318 STYLE 6

#### **Features**

- S and NSV Prefixes for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$	45	V
Collector - Base Voltage	$V_{CBO}$	50	V
Emitter – Base Voltage	$V_{EBO}$	5.0	V
Collector Current - Continuous	Ic	500	mAdc

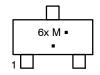
#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR- 5 Board, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2)  T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. FR-5 = 1.0 x 0.75 x 0.062 in.
- 2. Alumina =  $0.4 \times 0.3 \times 0.024$  in 99.5% alumina.

#### **MARKING DIAGRAM**



6x = Device Code x = A, B, or C M = Date Code\* = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

#### BC817-16L, SBC817-16L, BC817-25L, SBC817-25L, BC817-40L, SBC817-40L

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA)	V <sub>(BR)CEO</sub>	45	_	-	V
Collector – Emitter Breakdown Voltage (V <sub>EB</sub> = 0, I <sub>C</sub> = 10 μA)	V <sub>(BR)CES</sub>	50	-	-	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 1.0 μA)	V <sub>(BR)EBO</sub>	5.0	-	-	V
Collector Cutoff Current $(V_{CB} = 20 \text{ V})$ $(V_{CB} = 20 \text{ V}, T_A = 150^{\circ}\text{C})$	Ісво	_ _	- -	100 5.0	nA μA
ON CHARACTERISTICS					
DC Current Gain $ (I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}) \\ BC817-16, SBC817-16 \\ BC817-25, SBC817-25 \\ BC817-40, SBC817-40 \\ (I_C = 500 \text{ mA}, V_{CE} = 1.0 \text{ V}) $	h <sub>FE</sub>	100 160 250 40	- - -	250 400 600	-
	.,	40	_		.,
Collector – Emitter Saturation Voltage (I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA)	V <sub>CE(sat)</sub>	_	_	0.7	V
Base – Emitter On Voltage (I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 1.0 V)	V <sub>BE(on)</sub>	_	_	1.2	V
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	100	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	C <sub>obo</sub>	-	10	-	pF
SWITCHING CHARACTERISTICS					
Delay Time ( $V_{CC}$ = 3.0 Vdc, $V_{BE}$ = 0.5 V, $I_{C}$ = 10 mA)	t <sub>d</sub>	-	85	-	ns
Rise Time ( $V_{CC}$ = 3.0 Vdc, $V_{BE}$ = 0.5 V, $I_{C}$ = 10 mA)	t <sub>r</sub>	-	30	-	ns
Storage Time ( $V_{CC}$ = 3.0 Vdc, $I_C$ = 10 mA, $I_{B1}$ = 1 mA, $I_{B2}$ = 1 mA)	t <sub>s</sub>	-	1000	-	ns
Fall Time ( $V_{CC} = 3.0 \text{ Vdc}$ , $I_C = 10 \text{ mA}$ , , $I_{B1} = 1 \text{ mA}$ , $I_{B2} = 1 \text{ mA}$ )	t <sub>f</sub>	-	300	-	ns
Product parametric performance is indicated in the Electrical Characteristics	s for the listed test	conditions	unless of	herwise n	oted Produc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### ORDERING INFORMATION

Device	Specific Marking	Package	Shipping <sup>†</sup>	
BC817-16LT1G			3000 / Tape & Reel	
NSVBC817-16LT1G		SOT-23		
BC817-16LT3G	6A	(Pb-Free)	10.000 /T 0.D 1	
SBC817-16LT3G			10,000 / Tape & Reel	
BC817-25LT1G			3000 / Tape & Reel	
SBC817-25LT1G	0.0	SOT-23		
BC817-25LT3G	6B	(Pb-Free)	10 000 / Tara % Dayl	
SBC817-25LT3G			10,000 / Tape & Reel	
BC817-40LT1G			0000 / Tarra 9 Dani	
SBC817-40LT1G	00	SOT-23	3000 / Tape & Reel	
BC817-40LT3G	6C	(Pb-Free)	10 000 / Tara % Dayl	
SBC817-40LT3G			10,000 / Tape & Reel	

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### TYPICAL CHARACTERISTICS - BC817-16L, SBC817-16L

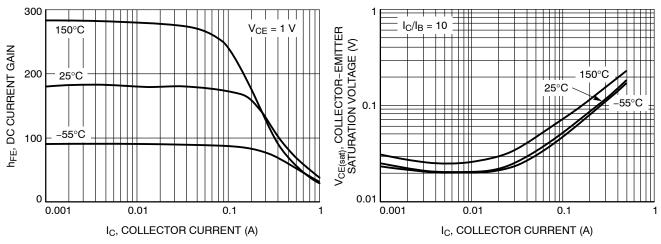


Figure 1. DC Current Gain vs. Collector Current

Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

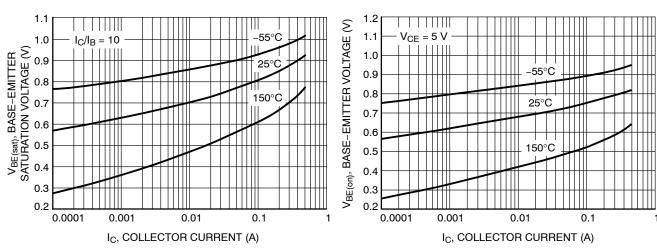


Figure 3. Base Emitter Saturation Voltage vs.
Collector Current

Figure 4. Base Emitter Voltage vs. Collector Current

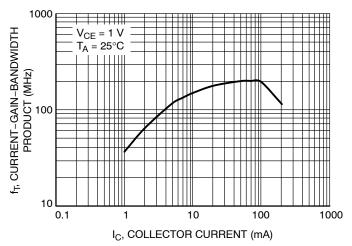
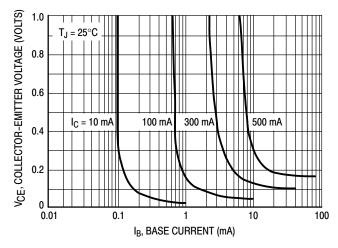


Figure 5. Current Gain Bandwidth Product vs. Collector Current

#### TYPICAL CHARACTERISTICS - BC817-16L, SBC817-16L



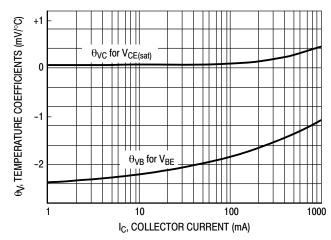


Figure 6. Saturation Region

**Figure 7. Temperature Coefficients** 

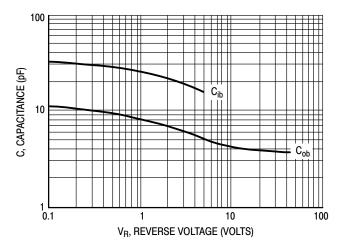


Figure 8. Capacitances

#### TYPICAL CHARACTERISTICS - BC817-25L, SBC817-25L

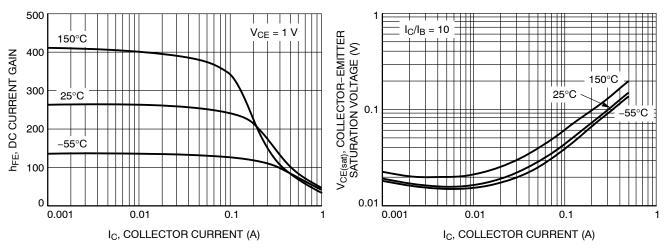


Figure 9. DC Current Gain vs. Collector Current

Figure 10. Collector Emitter Saturation Voltage vs. Collector Current

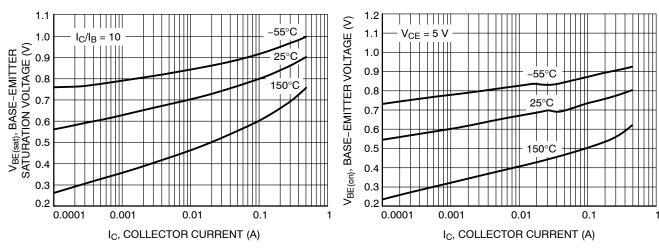


Figure 11. Base Emitter Saturation Voltage vs. Collector Current

Figure 12. Base Emitter Voltage vs. Collector Current

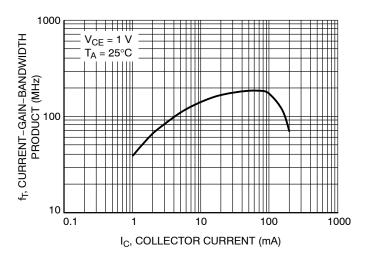
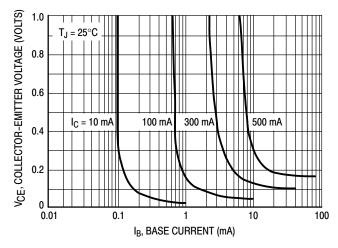


Figure 13. Current Gain Bandwidth Product vs. Collector Current

#### TYPICAL CHARACTERISTICS - BC817-25L, SBC81725L



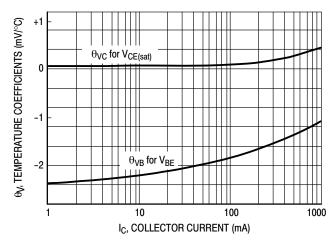


Figure 14. Saturation Region

Figure 15. Temperature Coefficients

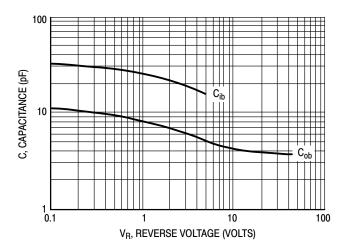


Figure 16. Capacitances

#### BC817-16L, SBC817-16L, BC817-25L, SBC817-25L, BC817-40L, SBC817-40L

#### TYPICAL CHARACTERISTICS - BC817-40L, SBC817-40L

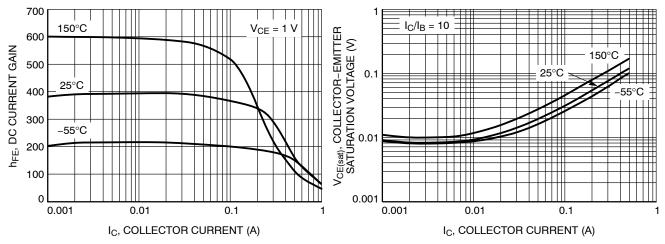


Figure 17. DC Current Gain vs. Collector Current

Figure 18. Collector Emitter Saturation Voltage vs. Collector Current

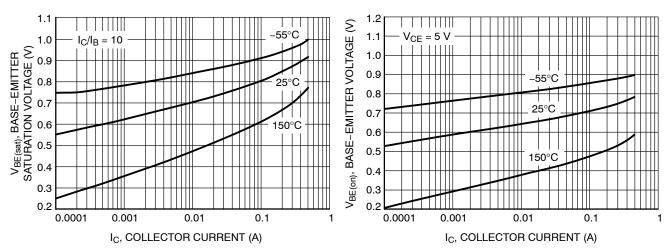


Figure 19. Base Emitter Saturation Voltage vs. Collector Current

Figure 20. Base Emitter Voltage vs. Collector Current

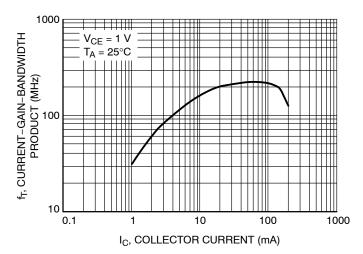
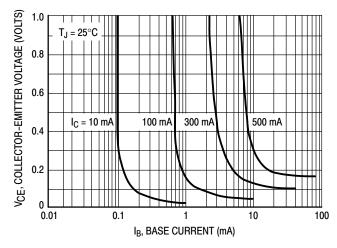


Figure 21. Current Gain Bandwidth Product vs. Collector Current

#### TYPICAL CHARACTERISTICS - BC817-40L, SBC817-40L



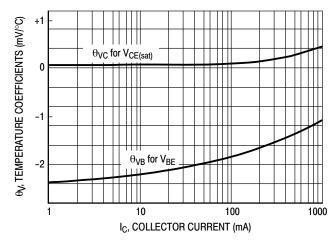


Figure 22. Saturation Region

Figure 23. Temperature Coefficients

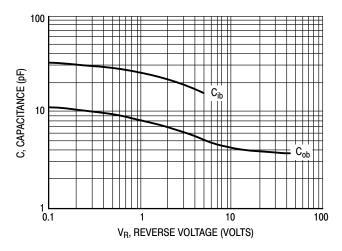


Figure 24. Capacitances

TYPICAL CHARACTERISTICS - BC817-16L, SBC817-16L, BC817-25L, SBC817-25L, BC817-40L, SBC817-40L

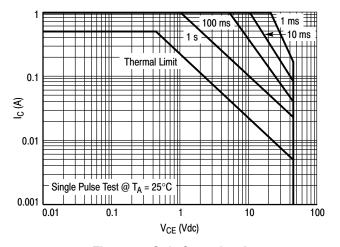


Figure 25. Safe Operating Area

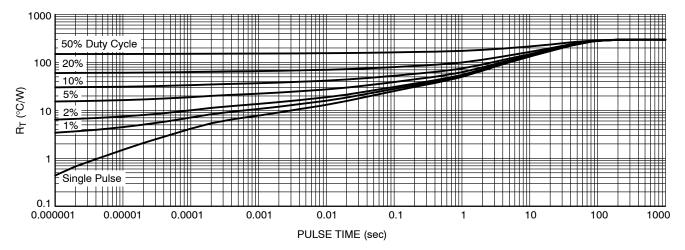


Figure 26. Thermal Response

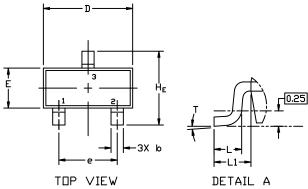




**SOT-23 (TO-236)** CASE 318 ISSUE AT

**DATE 01 MAR 2023** 









#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIM	ETERS		INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
Ε	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0*		10°	0*		10°

# GENERIC MARKING DIAGRAM\*



XXX = Specific Device Code

M = Date Code

■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

#### **STYLES ON PAGE 2**

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## MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



#### **SOT-23 (TO-236)** CASE 318 ISSUE AT

**DATE 01 MAR 2023** 

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	N	
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE		PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE		2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE		3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17: PIN 1. NO CONNECTION 2. ANODE 3. CATHODE	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE		PIN 1. NO CONNECTION	N PIN 1. CATHODE	PIN 1. CATHODE
2. CATHODE	2. CATHODE		2. CATHODE	2. ANODE	2. ANODE
3. ANODE	3. CATHODE		3. ANODE	3. CATHODE-ANODE	3. GATE
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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