

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS High-Voltage

NP0/X7R I KV TO 3 KV 0.47 pF to 33 nF

RoHS compliant & Halogen Free



YAGEO

14

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

<u>SCOPE</u>

This specification describes High-Voltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

PCs, Hard disk, Game PCs Power supplies LCD panel ADSL, Modem

<u>FEATURES</u>

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen Free compliant

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC <u>xxxx</u> <u>x</u> <u>x</u> <u>xxx</u> <u>x</u> B <u>x</u> <u>xxx</u> (1) (2) (3) (4) (5) (6) (7)

(I) SIZE – INCH BASED (METRIC)

0805 (2012) / 1206 (3216) / 1210 (3225) / 1808 (4520) / 1812 (4532)

(2) TOLERANCE

$C = \pm 0.25 \text{ pF}$
D = ±0.5 pF
G = ±2%
$J = \pm 5\%$
$K = \pm 10\%$
$M = \pm 20\%$

(3) PACKING STYLE

- R = Paper/PE taping reel; Reel 7 inch
- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch
- C = Bulk case

(4) TC MATERIAL

NPO X7R

(6)

(5) RATED VOLTAGE

C = KV		
D = 2 KV		
S = 2.5KV		
E = 3 KV		
PROCESS		
N = NPO		

B = Class 2 MLCC

(7) CAPACITANCE VALUE

2 significant digits+number of zeros

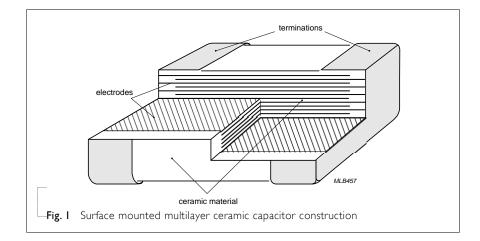
The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $|2| = |2 \times |0| = |20 \text{ pF}$

CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

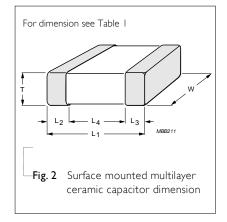


DIMENSION

Table I For outlines see fig. 2

TYPE	TYPE L _I (mm) W (mm) T		T (MM)	L ₂ / L ₃ (mm)	L ₄ (mm)
11115		•• (11111)	1 (1.11.1)	min.	max.	min.
0805	2.0 ±0.10	1.25 ±0.10	0.60 ±0.10 0.85 ±0.10	0.25	0.75	0.70
	2.0 ±0.20	1.25 ±0.20	1.25 ±0.20			
	3.2 ±0.15	1.60 ±0.15	0.60 ±0.10			
1206	3.2 ±0.30	1.60 ±0.20	0.85 ±0.10 1.25 ±0.20 1.60 ±0.20	0.25	0.75	1.40
	3.2 ±0.30	1.60 ±0.30	1.60 ±0.30			
1210	3.2 ±0.20	2.50 ±0.20	0.85 ±0.10 1.25 ±0.20	0.25	0.75	1.40
1210	3.2 ±0.30	2.50 ±0.20	1.60 ±0.20 2.00 ±0.20	0.25	0.75	1.40
1808	4.5 ±0.40	2.00 ±0.30	1.25 ±0.20 1.35 ±0.15 1.60 ±0.20 2.00 ±0.20	0.25	0.75	2.20
1812	4.5 ±0.40	3.20 ±0.20	0.85 ±0.10 1.25 ±0.20 1.35 ±0.15 1.60 ±0.20	0.25	0.75	2.20

OUTLINES



Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

CAPACITANCE RANGE & THICKNESS FOR NPO

Table 2	2 Sizes fro	om 0805 te	o 1812									
CAP.	0805	1206	• • • • •		1210	• • • • •	1808	• • • • •		1812	• • • •	
	I KV	I KV	2 KV	3 KV	I KV	2 KV	I KV	2 KV	3 KV	I KV	2 KV	3 KV
0.47 pF		0.85±0.1										
0.56 pF		0.85±0.1										
0.68 pF		0.85±0.1										
0.82 pF		0.85±0.1										
I.0 pF		0.85±0.1										
I.2 pF		0.85±0.1										
I.5 pF		0.85±0.1										
I.8 pF		0.85±0.1										
2.2 pF		0.85±0.1										
2.7 pF		0.85±0.1										
3.3 pF		0.85±0.1										
3.9 pF		0.85±0.1										
4.7 pF		0.85±0.1										
5.6 pF		0.85±0.1										
6.8 pF		0.85±0.1										
8.2 pF		0.85±0.1										
10 pF	0.85±0.1	0.85±0.1 1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
I2 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
15 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
18 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
22 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
27 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2					1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
33 pF	0.85±0.1	1.25±0.2	1.25±0.2	I.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
39 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
47 pF	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
56 pF	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
68 pF	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
82 pF	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
100 pF		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.25±0.2
120 pF		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2
150 pF		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	I.25±0.2	1.25±0.2
180 pF		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2

ΝΟΤΕ

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



CAPACITANCE RANGE & THICKNESS FOR NPO

Table 3	Sizes fro	om 0805 t	o 1812									
CAP.	0805	1206			1210		1808			1812		
	I KV	I KV	2 KV	3 KV	I KV	2 KV	I KV	2 KV	3 KV	I KV	2 KV	3 KV
220 pF		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.25±0.2
270 pF		1.25±0.2			1.25±0.2		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	
330 pF		1.25±0.2			1.25±0.2		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	
390 pF		I.25±0.2			1.25±0.2		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	
470 pF		1.25±0.2			1.25±0.2		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	
560 pF		1.25±0.2			1.25±0.2		1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	
680 pF		1.25±0.2			1.25±0.2		1.25±0.2			1.25±0.2	1.25±0.2	
820 pF		1.25±0.2			1.25±0.2					1.25±0.2	1.25±0.2	
I.0 nF		I.25±0.2			1.25±0.2					1.25±0.2	1.25±0.2	
I.2 nF										1.25±0.2		
I.5 nF										1.25±0.2		

NOTE

3. Values in shaded cells indicate thickness class in mm

4. Capacitance value of non E-12 series is on request



Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

CAPACITANCE RANGE & THICKNESS FOR X7R

Table 4												
CAP.	0805	1206			1210		1808			1812		
	I KV	I KV	2 KV	2.5KV	I KV	2 KV	I KV	2 KV	3 KV	I KV	2 KV	3 KV
100 pF												
150 pF	0.85±0.1								1.6±0.2			
220 pF	0.85±0.1	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2			1.6±0.2			
330 pF	0.85±0.1	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2		1.35±0.15	1.6±0.2			
470 pF	0.85±0.1	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.35±0.15	1.35±0.15	1.6±0.2			
680 pF	0.85±0.1	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.35±0.15	1.35±0.15	1.6±0.2			
I.0 nF	0.85±0.1	1.25±0.2	1.25±0.2	1.6±0.2	1.25±0.2	1.25±0.2	1.35±0.15	1.35±0.15	2.0±0.2	1.35±0.15	1.35±0.15	1.6±0.2
I.5 nF	1.25±0.2	1.25±0.2	1.25±0.2		1.25±0.2	1.25±0.2	1.35±0.15	1.35±0.15	2.0±0.2	1.35±0.15	1.35±0.15	
2.2 nF	1.25±0.2	1.25±0.2			1.25±0.2	1.60±0.2	1.35±0.15	1.6±0.2		1.35±0.15	1.35±0.15	
3.3 nF	1.25±0.2	1.25±0.2			1.25±0.2		1.35±0.15			1.35±0.15	1.35±0.15	
4.7 nF		1.25±0.2			1.25±0.2		1.35±0.15			1.35±0.15	1.35±0.15	
6.8 nF		1.25±0.2			1.25±0.2		1.6±0.2			1.35±0.15		
10 nF		1.25±0.2			1.25±0.2		1.6±0.2			1.35±0.15		
15 nF					1.25±0.2					1.35±0.15		
22 nF					1.6±0.2					1.35±0.15		
33 nF										1.6±0.2		
47 nF												
68 nF												
100 nF												

ΝΟΤΕ

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For products with 5% tolerance, please contact local sales force before ordering

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

THICKNESS CLASSES AND PACKING QUANTITY

	5		Ø180 MM		Ø330 MM		
SIZE CODE	THICKNESS CLASSIFICATION	TAPE WIDTH QUANTITY PER REEL	Paper	Blister	Paper	Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
0005	1.00 ±0.1 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
_	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
	1.15 ±0.15 mm	l2 mm		3,000			
	1.25 ±0.2 mm	l2 mm		3,000			
1808	1.35 ±0.15 mm	l2 mm		2,000			
1000	1.5 ±0.1 mm	l2 mm		2,000			
	1.6 ±0.2 mm	l2 mm		2,000			
	2.0 ±0.2 mm	l2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	l2 mm		2,000			
	1.15 ±0.1 mm	12 mm		1,000			
	1.15 ±0.15 mm	l2 mm		1,000			
	1.25 ±0.2 mm	l2 mm		1,000			
1812	1.35 ±0.15 mm	l2 mm		000, ا			
	1.5 ±0.1 mm	l2 mm		000, ا			
	1.6 ±0.2 mm	l2 mm		000, ا			
	2.0 ±0.2 mm	l2 mm		1,000			
	2.5 ±0.2 mm	l2 mm		500			

ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise stated all electrical values apply at an ambient temperature of 20 ± 1 °C, an atmospheric pressure of 86 to 106 kPa, and a relative humidity of 63 to 67%.

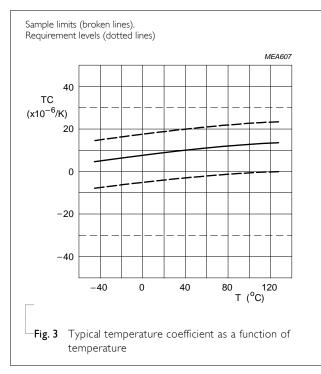
Table	e 6	
DESCRIF	PTION	VALUE
Capacita	nce range	10 pF to 33 nF
Capacita	nce tolerance	
NP0	C < 10 _P F	±0.25 pF, ±0.5 pF
	C ≥ 10 _P F	±2%, ±5%
X7R		±5% ⁽¹⁾ , ±10%
Dissipatio	on factor (D.F.)	
NP0	C < 30 _P F	≤ I / (400 + 20C)
	C ≥ 30 _P F	≤ 0.1 %
X7R		≤ 2.5 %
Insulatio	n resistance after 1 minute at U _r (DC)	$R_{ins} \geq$ 10 GQ or R_{ins} × C \geq 500 seconds whichever is less
	n capacitance change as a function of temperature ature characteristic/coefficient):	
NP0		±30 ppm/°C
X7R		±15%
Operatin	ng temperature range:	
NP0/X7	7R	−55 °C to +125 °C

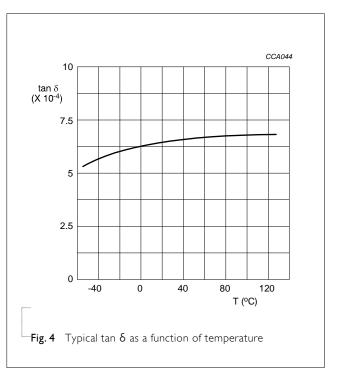
NOTE

1. ±5% tolerance of capacitance value isn't available for X7R full product range, please contact local sales force before ordering

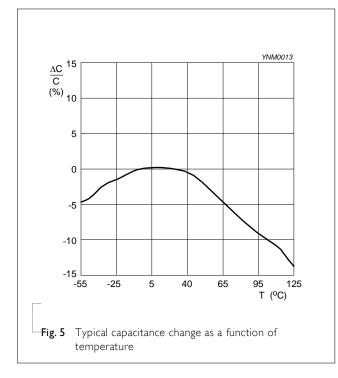


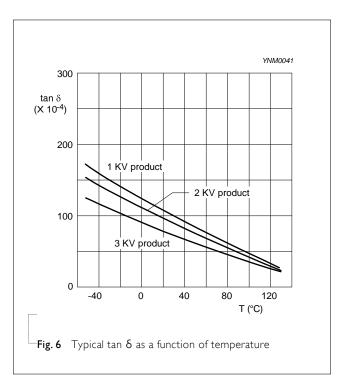
HIGH-VOLTAGE NP0





HIGH-VOLTAGE X7R





SOLDERING RECOMMENDATION

Table 7					
SOLDERING METHOD	SIZE 0402	0603	0805	1206	≥ 1210
Reflow	Reflow only	≥ I.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave		< 1.0 µF	< 2.2 µF	< 4.7 µF	

TESTS AND REQUIREMENTS

Table 8 Test procedures and requirements

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS		
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage		
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification		
Capacitance		4.5.1	Class 1: $f = 1$ MHz for C ≤ 1 nF, measuring at voltage 1 V _{rms} at 20 °C f = 1 KHz for C > 1 nF, measuring at voltage 1 V _{rms} at 20 °C Class 2: $f = 1$ KHz for C ≤ 10 µF, measuring at voltage 1 V _{rms} at 20 °C	Within specified tolerance		
Dissipation Factor (D.F.)		4.5.2	Class I: $f = 1$ MHz for C ≤ 1 nF, measuring at voltage 1 V _{rms} at 20 °C f = 1 KHz for C > 1 nF, measuring at voltage 1 V _{rms} at 20 °C Class 2: $f = 1$ KHz for C ≤ 10 µF, measuring at voltage 1 V _{rms} at 20 °C	In accordance with specification		
Insulation Resistance		4.5.3	$U_r \le 500$ V: At Ur for I minute $U_r > 500$ V: At 500 V for I minute	In accordance with specification		



 Surface-Mount Ceramic Multilayer Capacitors
 High-Voltage
 NP0/X7R
 1 KV to 3 KV

TEST MET	HOD	PROCEDURE	REQUIREMENTS
	4.6	Capacitance shall be measured by the steps shown in the following table. The capacitance change should be measured after 5 min at each specified temperature stage. $\boxed{Step \ Temperature(^{\circ}C)}$ a 25±2 b Lower temperature±3°C c 25±2 d Upper Temperature±2°C e 25±2 (1) Class I Temperature Coefficient shall be calculated from the formula as below $Temp, Coefficient = \frac{C2 - CI}{CI \times \Delta T} \times 10^{6} \text{ [ppm/°C]}$ C1: Capacitance at step c C2: Capacitance at 125°C ΔT : 100°C(=125°C-25°C) (2) Class II Capacitance Change shall be calculated from the formula as below $\Delta C = \frac{C2 - CI}{CI} \times 100\%$ C1: Capacitance at step c C2: Capacitance at step c C2: Capacitance at step c	<pre><general purpose="" series=""> Class1:</general></pre>
IEC 60384- 21/22	4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N
	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3 Conditions: bending 1 mm at a rate of 1 mm/s, radius jig 5 mm	No visible damage ΔC/C Class 1:
		IEC 60384- 4.7 21/22	in the following table. The capacitance change should be measured after 5 min at each specified temperature stage. $ \frac{\hline \text{Step} \overline{\text{Temperature(}^{\circ}\text{C})} \\ a 25\pm2 \\ b \text{Lower temperature} \pm 3^{\circ}\text{C} \\ c 25\pm2 \\ \hline d \text{Upper Temperature} \pm 2^{\circ}\text{C} \\ \hline e 25\pm2 \\ \hline (1) \text{ Class I} \\ \text{Temperature Coefficient shall be calculated from the formula as below} \\ \text{Temp, Coefficient } = \frac{C2 - C1}{C1 \times \Delta T} \times 10^{6} \text{ [ppm/}^{\circ}\text{C}\text{]} \\ C1: \text{ Capacitance at step c} \\ C2: \text{ Capacitance at step c} \\ C2: \text{ Capacitance at l25}^{\circ}\text{C} \\ \Delta \text{T: } 100^{\circ}\text{C}(=125^{\circ}\text{C}-25^{\circ}\text{C}) \\ \hline (2) \text{ Class II} \\ \text{Capacitance Change shall be calculated from the formula as below} \\ \Delta C = \frac{C2 - C1}{C1} \times 100^{\circ} \\ C1: \text{ Capacitance at step c} \\ C2: \text{ Capacitance at step be or d} \\ \text{IEC 60384-} 4.7 \\ \text{A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate} \\ 4.8 \\ \text{Mounting in accordance with IEC 60384-22 paragraph 4.3} $

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TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Resistance to Soldering Heat		4.9	Precondition: $150 \pm 0/-10$ °C for 1 hour, then keep for 24 ± 1 hours at room temperature Preheating: for size ≤ 1206 : 120 °C to 150 °C for 1 minute	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned $\Delta C/C$
			Preheating: for size >1206: 100 °C to 120 °C for 1 minute and 170 °C to 200 °C for 1 minute Solder bath temperature: 260 ±5 °C Dipping time: 10 ±0.5 seconds Recovery time: 24 ±2 hours	Class 1: NP0: within ±0.5% or 0.5 pF, whichever is greate Class2: X7R: ±10%
				D.F. within initial specified value R _{ins} within initial specified value
Solderability		4.10	Preheated to a temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			 Temperature: 235±5°C / Dipping time: 2 ±0.5 s Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free)Depth of immersion: 10mm 	
Rapid Change	IEC 60384-	4.11	Preconditioning;	No visual damage
of Temperature	21/22		150 +0/–10 °C for 1 hour, then keep for 24 ±1 hours at room temperature	ΔC/C Class I:
			5 cycles with following detail: 30 minutes at lower category temperature 30 minutes at upper category temperature	NP0: within ±1% or 1 pF, whichever is greater Class2: X7R: ±15%
			Recovery time 24 ±2 hours	D.F. meet initial specified value R _{ins} meet initial specified value
Damp Heat		4.13	I. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for -	No visual damage after recovery
			24 ± 1 hour at room temp	$\Delta C/C$
			2. Initial measure:	Class I:
			Spec: refer to initial spec C, D, IR	NP0: within $\pm 2\%$ or 1 pF, whichever is greater
			3. Damp heat test:	Class2:
			500 \pm 12 hours at 40 \pm 2 °C;	X7R: ±15%
			90 to 95% R.H.	D.F.
			4. Recovery:	Class I:
			Class 1: 6 to 24 hours	NP0: $\leq 2 \times \text{specified value}$
			Class 2: 24 \pm 2 hours	Class2: $(2 - 2)^{2} = (2 - 2)^{2}$
			5. Final measure: C, D, IR	$X7R: \ge 25 V: \le 5\%$
			P.S. If the capacitance value is less than the	R _{ins}
			minimum value permitted, then after the other measurements have been made the capacitor shall	Class 1: NP0: \geq 2,500 M Ω or R _{ins} \times C _r \geq 25s whichever is less
			be preconditioned according to <i>"IEC 60384 4.1"</i> and then the requirement shall be met.	Class2: X7R: \geq 500 M Ω or R _{ins} \times C _r \geq 25s whichever is less

Surface-Mount Ceramic Multilayer Capacitors	High-Voltage	NP0/X7R	1 KV to 3 KV	l
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TEST	TEST METH	HOD	PROCEDURE			REQUIREMENTS	
Endurance	IEC 60384- 21/22	4.14	I. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp 2. Initial measure: Spec: refer to initial spec C, D, IR Endurance test:		en keep for -	REQUIREMENTS No visual damage ΔC/C Class1: NP0: within ±2% or 1 pF, whichever is greater Class2:	
			Temperature: NP0/X7R: 125 °C Specified stress voltage applied for 1,000 hours. High-Voltage series follows the stress conditions below:			×7R: ±15% D.F. Class I: NP0: ≤ 2 × specified value	
			Voltage	NPO	X7R	Class2:	
			≤ 100V	2.0 x Ur	2.0 × Ur	X7R: ≥ 25 V: ≤ 5%	
			200/250V	1.5 x Ur	1.5 x Ur	R _{ins}	
			500/630V	1.3 x Ur	1.2 x Ur	Class I:	
			\geq 1 KV	I.2 x Ur	I.I x Ur	NP0: \geq 4,000 M Ω or R _{ins} x C _r \geq 40s whichever is less	
			 3. Recovery time: 24 ±2 hours 4. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned according to <i>"IEC 60384 4.1"</i> and then the requirement shall be met. 			Class2:	
						X7R: \geq 1,000 M Ω or R _{ins} x C _r \geq 50s whichever is less	
Voltage Proof			Specified stress	voltage applie	d for 1~5 seconds	No breakdown or flashover	
			Ur ≤ 100 V: ser	ies applied 2.5	i Ur		
			100 V < Ur ≤ 2 (1.5 Ur + 100)	200 V series ap	pplied		
			200 V < Ur ≤ 500 V series applied (1.3 Ur + 100) Ur > 500 V: 1.3 Ur Ur ≥ 1KV: 1.2 Ur				
			Charge/Dischar	ge current less	s than 50mA		

 Surface-Mount Ceramic Multilayer Capacitors
 High-Voltage
 NP0/X7R
 1 KV to 3 KV

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Version 13 Feb. 28, 2021 - - Add NPO/1206/0.47pF to 10pF with 0.85 mm Version 12 Dec. 01, 2020 - - Add X7R/0805/1.5nF to 3.3nF/1KV. NPO/0805/ 56pF to 82pF/1KV Version 11 Jul. 13, 2018 - - Add NPO/1206/10pF to 47pF/3KV Version 10 Mar. 7, 2017 - - 0805 L4 spec updated Version 8 Oct. 12, 2015 - - Version 7 May 21, 2014 - - Version 6 Jun. 17, 2012 - - Version 7 Sep 25, 2012 - - Version 7 Aug 08, 2011 - - Version 7 Jun 19, 2011 - - Version 8 Sep 25, 2012 - - Version 1 Jun 19, 2011 - - Version 2 Feb 02, 2010 - - Version 2 Feb 02, 2010 - - Version 1 Sep 30, 2005 - - Version 1 Sep 30, 2005 - - Version 1 Sep 30, 2005 - - Version 1 Sep 30, 2005 <td< th=""><th>REVISION</th><th>DATE</th><th>CHANGE NOTIFICATION</th><th>DESCRIPTION</th></td<>	REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 11 Jul. 13, 2018 - - Add NPO/1206/10pF to 47pF/3KV Version 10 Mar. 7, 2017 - - 0805 L4 spec updated Version 9 Jan. 16, 2017 - - Product range updated Version 8 Oct. 12, 2015 - - Version 7 May 21, 2014 - - Version 6 Jun. 17, 2012 - - Version 5 Sep 25, 2012 - - Version 3 Jan 19, 2011 - - Version 2 Feb 02, 2010 - - Version 2 Feb 02, 2010 - - Version 1 Sep 30, 2005 - - Version 1 Sep 30, 2005 - -	Version 13	Feb. 28, 2021	-	- Add NPO/1206/0.47pF to 10pF with 0.85 mm
Version 10 Mar. 7, 2017 - - 0805 L4 spec updated Version 9 Jan. 16, 2017 - - Product range updated Version 8 Oct. 12, 2015 - - Version 7 May 21, 2014 - - Version 6 Jun. 17, 2012 - - Version 5 Sep 25, 2012 - - Version 4 Aug 08, 2011 - - Version 3 Jan 19, 2011 - - Version 2 Feb 02, 2010 - - Version 2 Feb 02, 2010 - - Version 4 Sep 30, 2005 - - Version 1 Sep 30, 2005 - -	Version 12	Dec. 01, 2020	-	- Add X7R/0805/1.5nF to 3.3nF/1KV. NPO/0805/ 56pF to 82pF/1KV
Version 9 Jan. 16, 2017 - - Product range updated Version 8 Oct. 12, 2015 - - Product range updated Version 7 May 21, 2014 - - Product range updated Version 6 Jun. 17, 2012 - - Product range updated Version 5 Sep 25, 2012 - - Product range updated Version 4 Aug 08, 2011 - - Product range updated Version 3 Jan 19, 2011 - - Dimension updated Version 2 Feb 02, 2010 - - Change to dual brand datasheet that describe High-Voltage NP0/X7R series with RoHS compliant Replace the high voltage part of pdf files: UP-NP0X7R_HV_IK-to-4KV_I and UY-NP0X7R_HV_IK-to-4KV_I Description of "Halogen Free compliant" added - Product range updated - Define global part number - Test method and procedure updated Version 1 Sep 30, 2005 - - Thickness revised	Version 11	Jul. 13, 2018	-	- Add NPO/1206/10pF to 47pF/3KV
Version 8 Oct. 12, 2015 - - Product range updated Version 7 May 21, 2014 - - Product range updated Version 6 Jun. 17, 2012 - - Product range updated Version 5 Sep 25, 2012 - - Product range updated Version 4 Aug 08, 2011 - - Product range updated Version 3 Jan 19, 2011 - - Product range updated Version 2 Feb 02, 2010 - - Other transform updated - - - Dimension updated - - - Change to dual brand datasheet that describe High-Voltage NP0/X7R Version 2 Feb 02, 2010 - - Change to dual brand datasheet that describe High-Voltage NP0/X7R Version 2 Feb 02, 2010 - - Change to dual brand datasheet that describe High-Voltage NP0/X7R - Replace the high voltage part of pdf files: UP-NP0X7R_HV_IK-to-4KV_I and UY-NP0X7R_HV_IK-to-4KV_I and UY-NP0X7R_IK-to-4KV_I and UY-NP0X7R_IK-to-4KV	Version 10	Mar. 7, 2017	-	- 0805 L4 spec updated
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Version 5 Sep 25, 2012 - - Product range updated Version 4 Aug 08, 2011 - - Product range updated Version 3 Jan 19, 2011 - - Dimension updated - - Add NP0 0805 1KV - - Version 2 Feb 02, 2010 - - Change to dual brand datasheet that describe High-Voltage NP0/X7R series with RoHS compliant - Replace the high voltage part of pdf files: UP-NP0X7R_HV_1K-to-4KV_1 and UY-NP0X7R_HV_1K-to-4KV_1 - Description of "Halogen Free compliant" added - - Product range updated - - Define global part number - - Test method and procedure updated - Version 1 Sep 30, 2005 -	Version 7	May 21, 2014	-	- Product range updated
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- Define global part number - Test method and procedure updated Version I Sep 30, 2005 Thickness revised				- Product range updated
- Test method and procedure updated Version 1 Sep 30, 2005 Thickness revised				5
Version 0 Sep 12, 2005 New	Version I	Sep 30, 2005	-	- Thickness revised
	Version 0	Sep 12, 2005	-	- New

Surface-Mount Ceramic Multilayer Capacitors

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