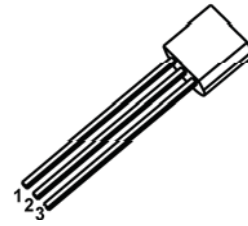
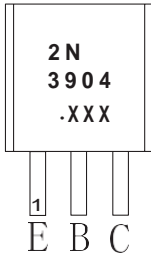


**BIPOLAR TRANSISTOR (NPN)**
**FEATURE**

- NPN silicon epitaxial planar transistor for switching and Amplifier applications
- As complementary type, the PNP transistor 2N3906 is Recommended
- This transistor is also available in the SOT-23 case with the type designation MMBT3904


**TO - 92**

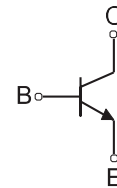
1. EMITTER 2. BASE 3. COLLECTOR

**MARKING**


2N3904=Device code

Solid dot=Green molding compound device, if none, the normal device

XXX=Code

**Equivalent Circuit**

**ORDERING INFORMATION**

Part Number	Package	Packing Method	Pack Quantity
2N3904	TO-92	Bulk	1000pcs/Bag
2N3904-TA	TO-92	Tape	2000pcs/Box

**MAXIMUM RATINGS (T<sub>a</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Value	Unit
V <sub>CB0</sub>	Collector-Base Voltage	60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>c</sub>	Collector Current -Continuous	0.2	A
P <sub>c</sub>	Collector Power Dissipation	0.625	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature	-55-150	°C

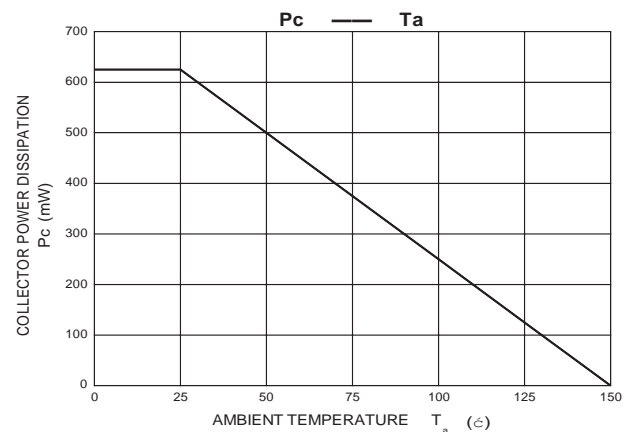
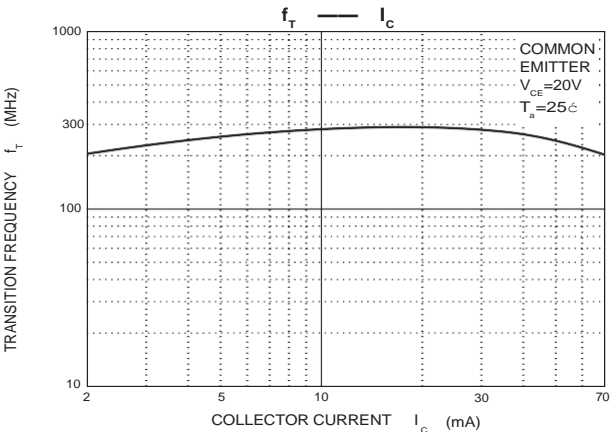
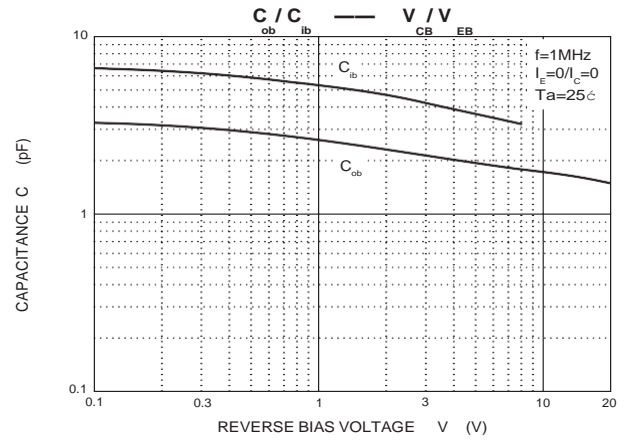
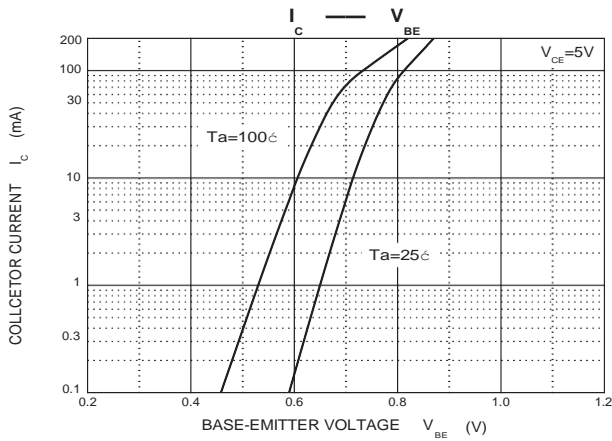
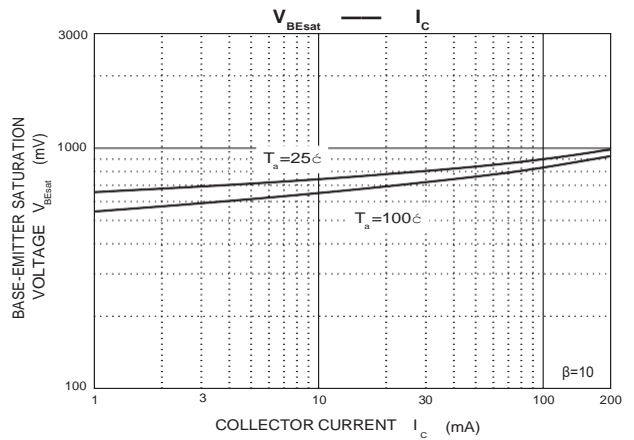
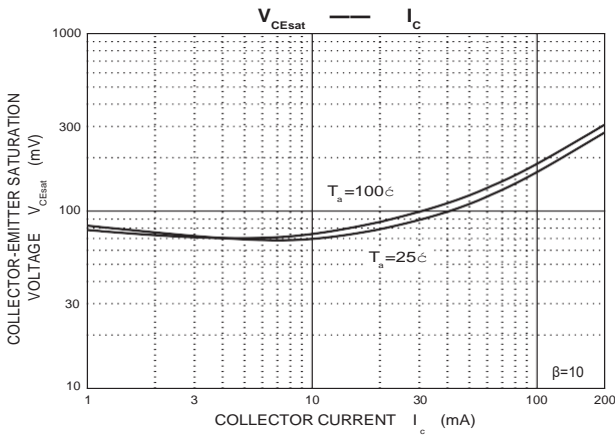
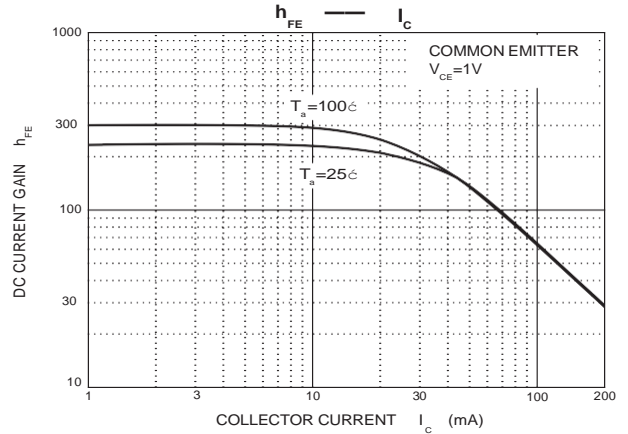
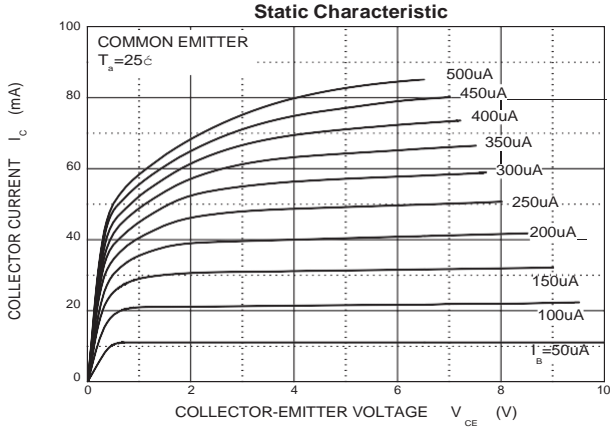
**ELECTRICAL CHARACTERISTICS T<sub>a</sub>=25 °C unless otherwise specified**

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> =10μA, I <sub>E</sub> =0	60			V
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 1mA, I <sub>B</sub> =0	40			V
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 10μA, I <sub>C</sub> =0	6			V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> =60V, I <sub>E</sub> =0			0.1	μA
Collector cut-off current	I <sub>CEX</sub>	V <sub>CE</sub> =30V, V <sub>EB(off)</sub> =3V			0.05	μA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 5V, I <sub>C</sub> =0			0.1	μA
DC current gain	h <sub>FE1</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =10mA	100		400	
	h <sub>FE2</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =50mA	60			
	h <sub>FE3</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =100mA	30			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.3	V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.95	V
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> =20V, I <sub>C</sub> =10mA, f=100MHz	300			MHz
Delay Time	t <sub>d</sub>	V <sub>CC</sub> =3V, V <sub>BE</sub> =0.5V,			35	ns
Rise Time	t <sub>r</sub>	I <sub>C</sub> =10mA, I <sub>B1</sub> =1mA			35	ns
Storage Time	t <sub>s</sub>	V <sub>CC</sub> =3V, I <sub>C</sub> =10mA			200	ns
Fall Time	t <sub>f</sub>	I <sub>B1</sub> =I <sub>B2</sub> =1mA			50	ns

**CLASSIFICATION OF h<sub>FE1</sub>**

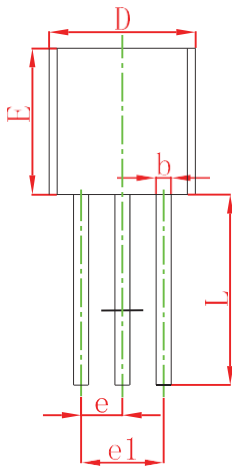
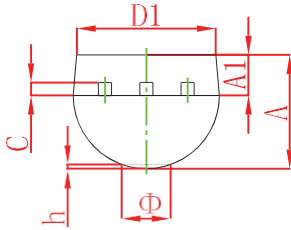
Rank	O	Y	G
Range	100-200	200-300	300-400

Typical Characteristics



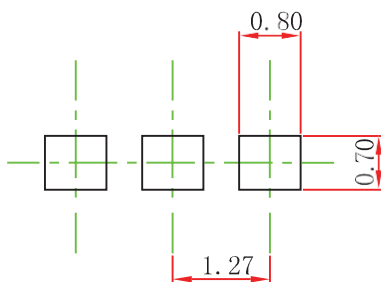
BIPOLAR TRANSISTOR (NPN)

TO-92 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	3.300	3.700	0.130	0.146
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
c	0.360	0.510	0.014	0.020
D	4.300	4.700	0.169	0.185
D1	3.430		0.135	
E	4.300	4.700	0.169	0.185
e	1.270 TYP		0.050 TYP	
e1	2.440	2.640	0.096	0.104
L	14.100	14.500	0.555	0.571
Φ		1.600		0.063
h	0.000	0.380	0.000	0.015

TO-92 Suggested Pad Layout



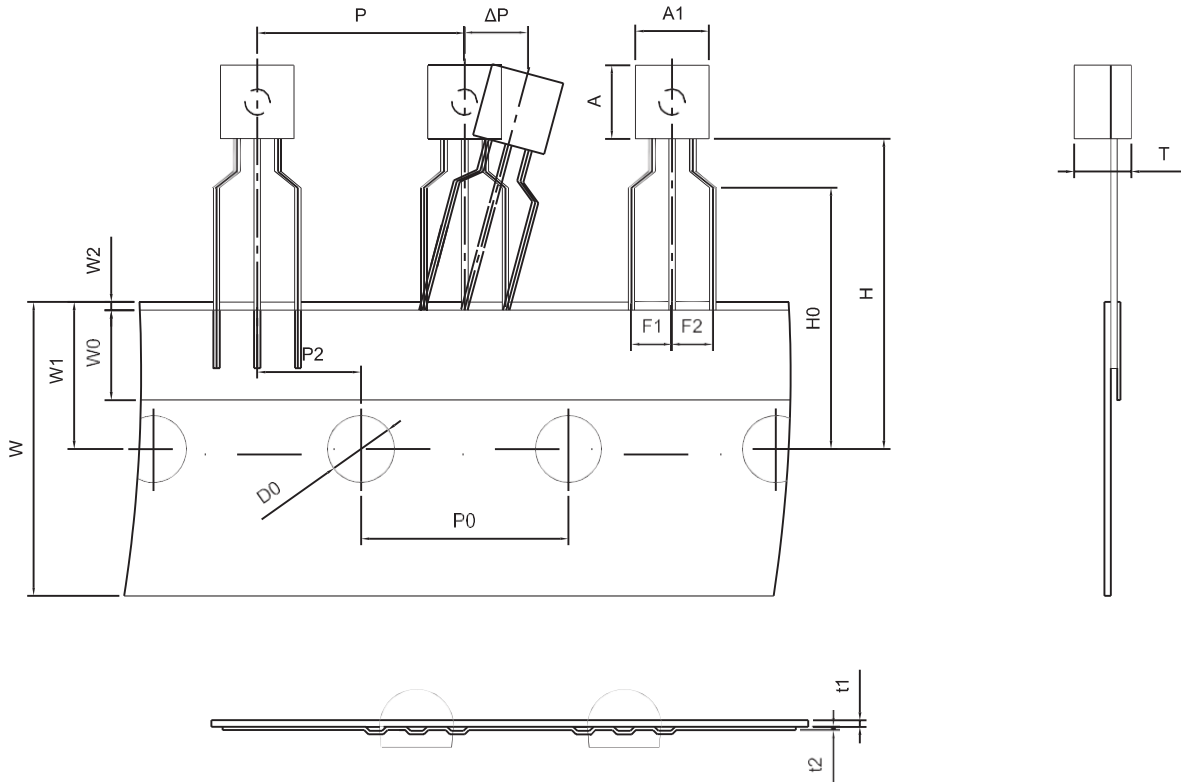
Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05mm.
3. The pad layout is for reference purposes only.

**BIPOLAR TRANSISTOR (NPN)**

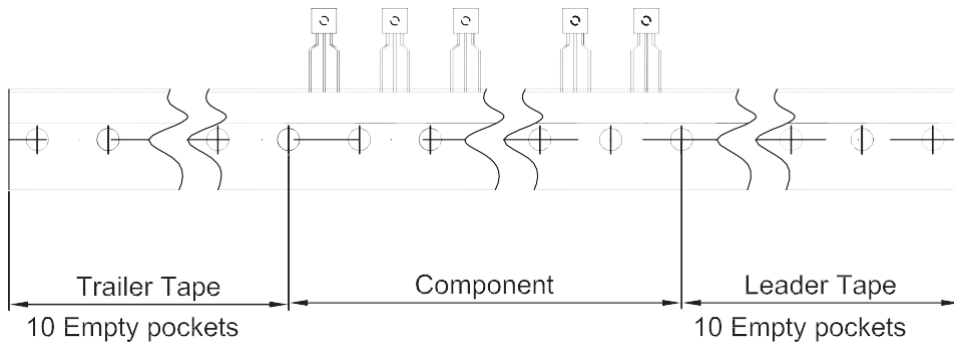
**TO-92 7DSH DQG 5HH0**

**TO-92 PACKAGE TAPEING DIMENSION**



Dimensions are in millimeter

A1	A	T	P	P0	P2	F1	F2	W
4.5	4.5	3.5	12.7	12.7	6.35	2.5	2.5	18.0
W0	W1	W2	H	H0	D0	t1	t2	ΔP
6.0	9.0	1.0 MAX.	19.0	16.0	4.0	0.4	0.2	0



Package	Box	Box Size(mm)	Carton	Carton Size(mm)
TO-92	2000 pcs	333×162×43	20,000 pcs	350×340×250