To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LS247

BCD-to-Seven-Segment Decoder / Driver (with three-state outputs)

REJ03D0465–0300
Rev.3.00
Jul.15.2005

The HD74LS247 is electrically and functionally identical to the HD74LS47, respectively, and has the same pin assignments as its equivalents.

It can be used interchangeably in present or future designs to offer designers a choice between two indicator fonts. The HD74LS47 composes the 6 and the 9 without tails and the HD74LS247 composes the 6 and the 9 with tails. Composition of all other characters, including display patterns for BCD inputs above nine, is identical. The HD74LS247 features active-low outputs designed for driving indicators directly. All of the circuits have full ripple-blanking input / output controls and a lamp test input.

Segment identification and resultant displays are shown below. Display patterns for BCD input conditions. This circuit incorporates automatic leading and / or trailing-edge zero-blanking control (RBI and RBO). Lamp test (LT) of this type may be performed at any time when the BI / RBO node is at a high level.

This type contains an overriding blanking input (BI) which can be used to control the lamp intensity be pulsing or to inhibit the outputs.

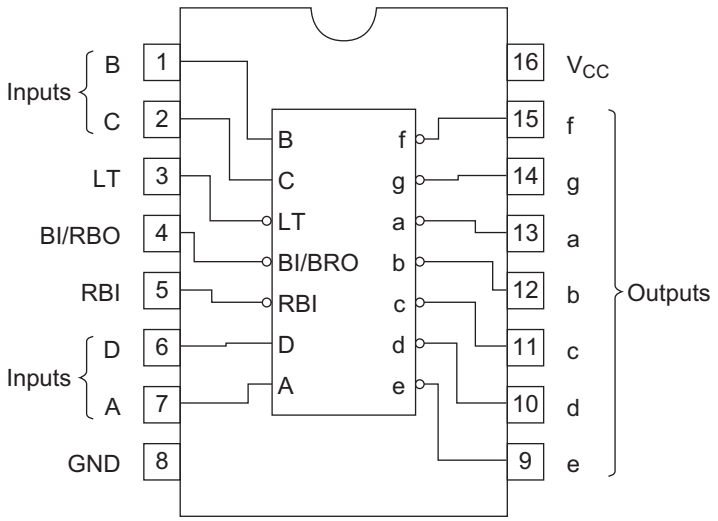
Features

- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS247P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	P	—
HD74LS247FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS247RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



(Top view)

Function Table

Decimal or Function	Inputs						BI/ RBO	Outputs							Note
	LT	RBI	D	C	B	A		a	b	c	d	e	f	g	
0	H	H	L	L	L	L	H	ON	ON	ON	ON	ON	ON	OFF	1
1	H	X	L	L	L	H	H	OFF	ON	ON	OFF	OFF	OFF	OFF	
2	H	X	L	L	H	L	H	ON	ON	OFF	ON	ON	OFF	ON	
3	H	X	L	L	H	H	H	ON	ON	ON	ON	OFF	OFF	ON	
4	H	X	L	H	L	L	H	OFF	ON	ON	OFF	OFF	ON	ON	
5	H	X	L	H	L	H	H	ON	OFF	ON	ON	OFF	ON	ON	
6	H	X	L	H	H	L	H	ON	OFF	ON	ON	ON	ON	ON	
7	H	X	L	H	H	H	H	ON	ON	ON	OFF	OFF	OFF	OFF	
8	H	X	H	L	L	L	H	ON	ON	ON	ON	ON	ON	ON	
9	H	X	H	L	L	H	H	ON	ON	ON	ON	OFF	ON	ON	
10	H	X	H	L	H	L	H	OFF	OFF	OFF	ON	ON	OFF	ON	
11	H	X	H	L	H	H	H	OFF	OFF	ON	ON	OFF	OFF	ON	
12	H	X	H	H	L	L	H	OFF	ON	OFF	OFF	OFF	ON	ON	
13	H	X	H	H	L	H	H	ON	OFF	OFF	ON	OFF	ON	ON	
14	H	X	H	H	H	L	H	OFF	OFF	OFF	ON	ON	ON	ON	
15	H	X	H	H	H	H	H	OFF	OFF	OFF	OFF	OFF	OFF	OFF	
BI	X	X	X	X	X	X	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	2
RBI	H	L	L	L	L	L	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	3
LT	L	X	X	X	X	X	H	ON	ON	ON	ON	ON	ON	ON	4

H; high level, L; low level, X; irrelevant

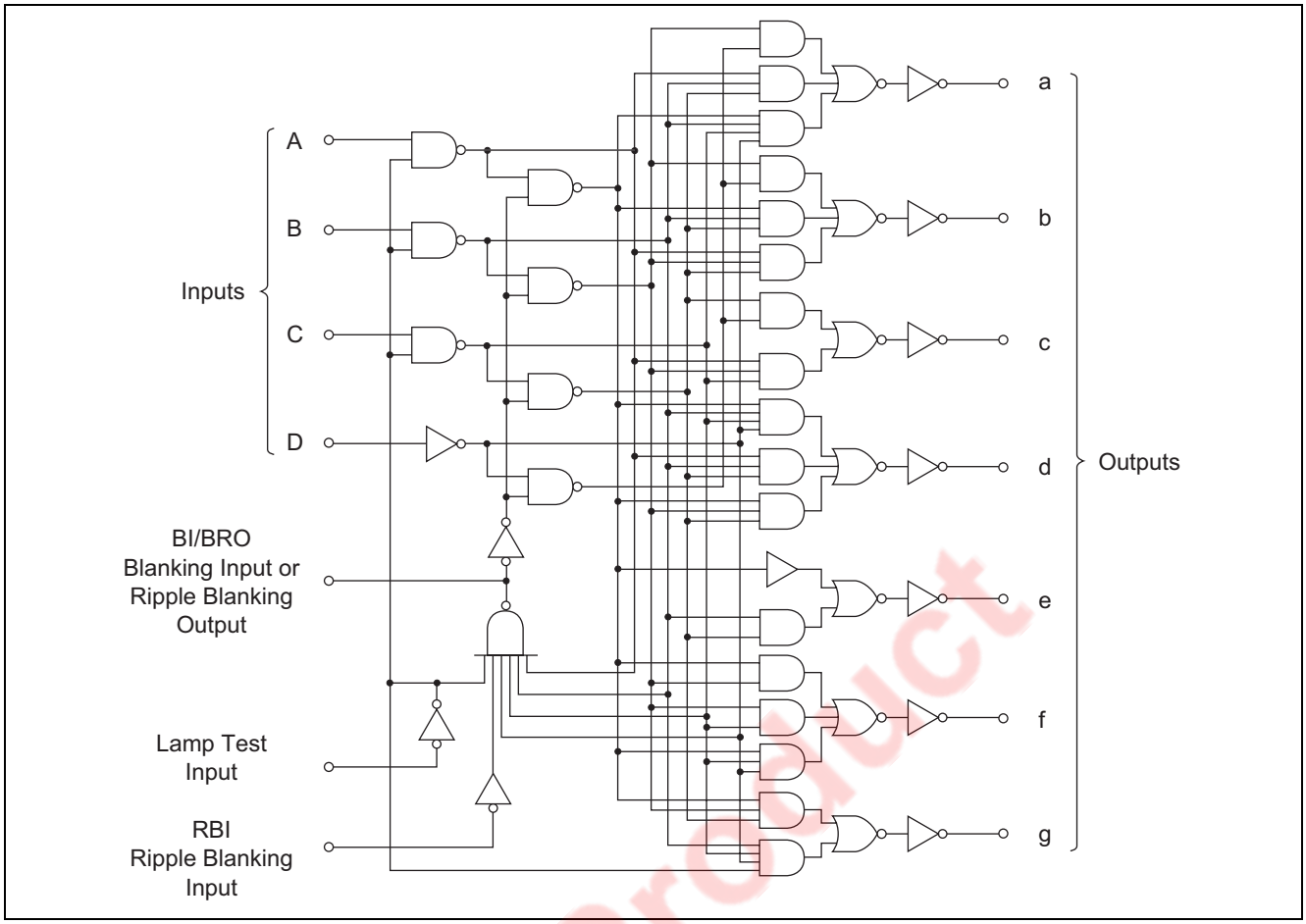
Notes: 1. The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The ripple-blanking input (RBI) must be open or high if blanking of a decimal zero is not desired.

2. When a low logic level is applied directly to the blanking input (BI), all segment outputs are off regardless of the level of any other input.

3. When ripple-blanking input (RBI) and inputs A, B, C, and D are a low level with the lamp test input high, all segment outputs go off and the ripple-blanking output (RBO) goes to a low level (response condition).

4. When a blanking input ripple blanking input (BI/RBO) is open or held high and a low is applied to the lamp-test input, all segment outputs are on.

Block Diagram



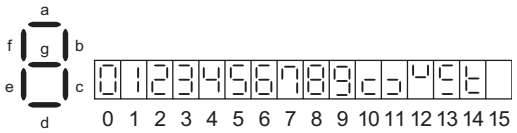
Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V_{CC}	7	V
Input voltage	V_{IN}	7	V
Output current ($t_w \leq 1$ms, duty cycle $\leq 10\%$)	$I_{O\ (peak)}$	200	mA
Output current (off-state)	$I_{O\ (off)}$	1	mA
Operating temperature	Topr	–20 to +75	°C
Power dissipation	P_T	400	mW
Storage temperature	Tstg	–65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item		Symbol	Min	Typ	Max	Unit
Supply voltage		V_{CC}	4.75	5.00	5.25	V
Operating temperature		Topr	–20	25	75	°C
Output voltage	a to g	$V_{O (off)}$	—	—	15	V
Output current	a to g	$I_{O (on)}$	—	—	24	mA
	BI / RBO	I_{OH}	—	—	-50	µA
	BI / RBO	I_{OL}	—	—	3.2	mA



Electrical Characteristics

(Ta = –20 to +75 °C)

Item		Symbol	min.	typ.*	max.	Unit	Condition	
Input voltage		V_{IH}	2.0	—	—	V		
		V_{IL}	—	—	0.8	V		
Output voltage	BI/RBO	V_{OH}	2.4	—	—	V	V_{CC} = 4.75 V, V_{IH} = 2 V, V_{IL} = 0.8 V, I_{OH} = –50 µA	
	BI/RBO	V_{OL}	—	—	0.4	V	I_{OL} = 1.6 mA	V_{CC} = 4.75 V, V_{IH} = 2 V, V_{IL} = 0.8 V
			—	—	0.5		I_{OL} = 3.2 mA	
Output current	a to g	$I_{O (off)}$	—	—	250	µA	V_{CC} = 5.25 V, V_{IH} = 2 V, V_{IL} = 0.8 V, $V_{O (off)}$ = 15 V	
Output voltage	a to g	$V_{O (on)}$	—	—	0.4	V	$I_{O (on)}$ = 12 mA	V_{CC} = 5.25 V, V_{IH} = 2 V, V_{IL} = 0.8 V
			—	—	0.5		$I_{O (on)}$ = 24 mA	
Input current		I_{IH}	—	—	20	µA	V_{CC} = 5.25 V, V_I = 2.7 V	
	Except BI/RBO	I_{IL}	—	—	–0.4	mA	V_{CC} = 5.25 V, V_I = 0.4 V	
	BI/RBI		—	—	–1.2			
		I_I	—	—	0.1	mA	V_{CC} = 5.25 V, V_I = 7 V	
Short-circuit output current	BI/RBO	I_{OS}	–0.3	—	–2	mA	V_{CC} = 5.25 V	
Supply current**		I_{CC}	—	7	13	mA	V_{CC} = 5.25 V	
Input clamp voltage		V_{IK}	—	—	–1.5	V	V_{CC} = 4.75 V, I_{IN} = –18 mA	

Notes: * V_{CC} = 5 V, Ta = 25°C

** I_{CC} is measured with all outputs open and all inputs at 4.5 V.

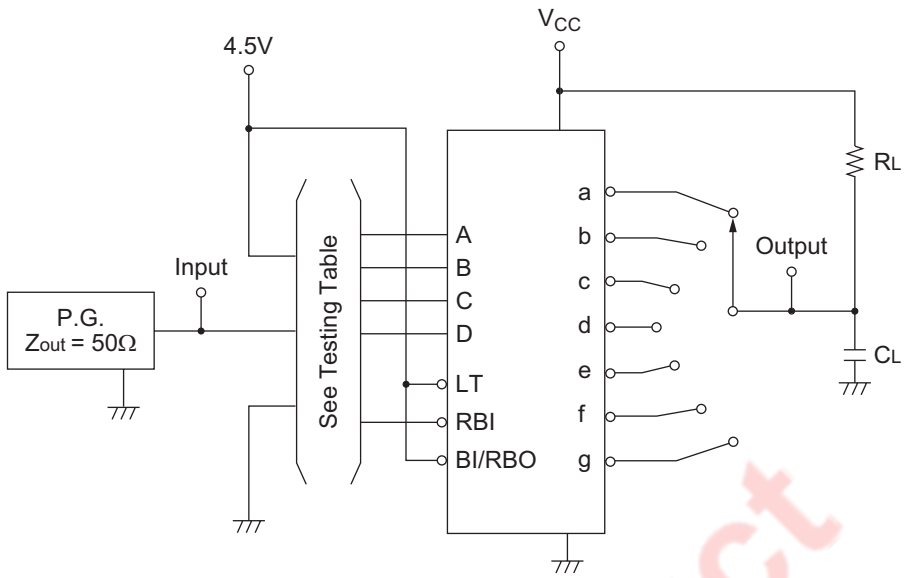
Switching Characteristics

(V_{CC} = 5 V, Ta = 25°C)

Item	Symbol	Input	min.	typ.	max.	Unit	Condition
Turn-on time	t_{on}	A	—	—	100	ns	C_L = 15 pF, R_L = 665 Ω
		RBI	—	—	100		
Turn-off time	t_{off}	A	—	—	100	ns	
		RBI	—	—	100		

Testing Method

Test Circuit

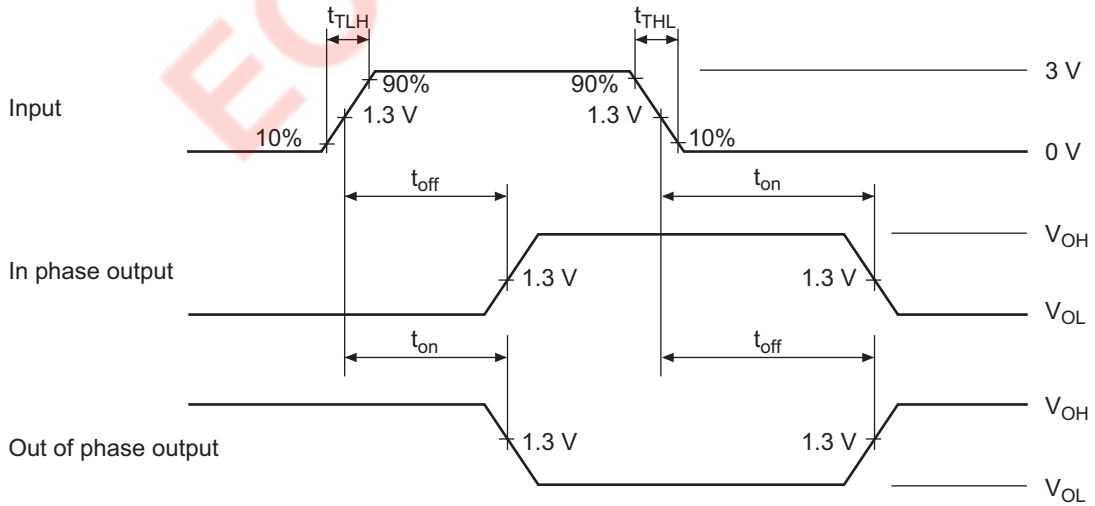


Note: C_L includes probe and jig capacitance.

Testing Table

Item	Inputs					Outputs						
	RBI	D	C	B	A	a	b	c	d	e	f	g
t_{on} / t_{off}	4.5V	GND	GND	GND	IN	OUT	—	—	OUT	OUT	OUT	—
	4.5V	GND	GND	4.5V	IN	—	—	OUT	—	OUT	—	—
	4.5V	GND	4.5V	4.5V	IN	—	OUT	—	OUT	OUT	OUT	OUT
	IN	GND	GND	GND	GND	OUT	OUT	OUT	OUT	OUT	OUT	—

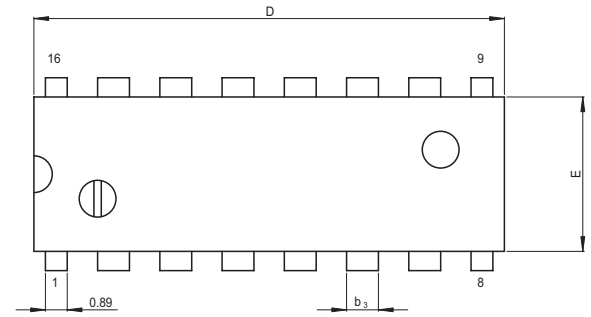
Waveform

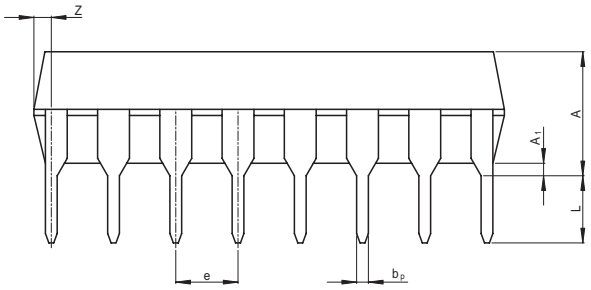


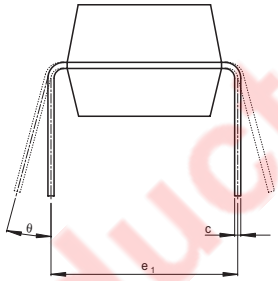
Note: Input pulse; $t_{TLH} \leq 15$ ns, $t_{THL} \leq 6$ ns, PRR = 1 MHz, duty cycle = 50%

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-DIP16-6.3x19.2-2.54	PRDP0016AE-B	DP-16FV	1.05g



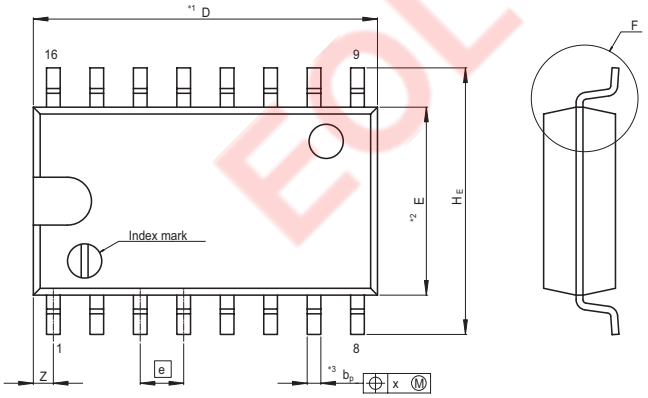


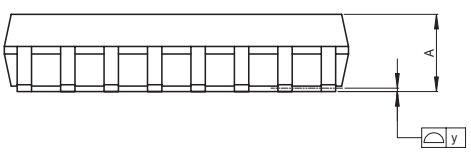


(Ni/Pd/Au plating)

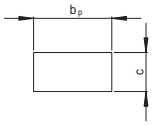
Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
e 1	——	7.62	——
D	——	19.2	20.32
E	——	6.3	7.4
A	——	——	5.06
A 1	0.51	——	——
b p	0.40	0.48	0.56
b 3	——	1.30	——
c	0.19	0.25	0.31
θ	0°	——	15°
e	2.29	2.54	2.79
Z	——	——	1.12
L	2.54	——	——

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP16-5.5x10.06-1.27	PRSP0016DH-B	FP-16DAV	0.24g

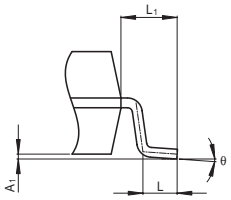




NOTE)
1. DIMENSIONS"*1 (Nom)"AND"*2"
   DO NOT INCLUDE MOLD FLASH.
2. DIMENSION"*3"DOES NOT
   INCLUDE TRIM OFFSET.



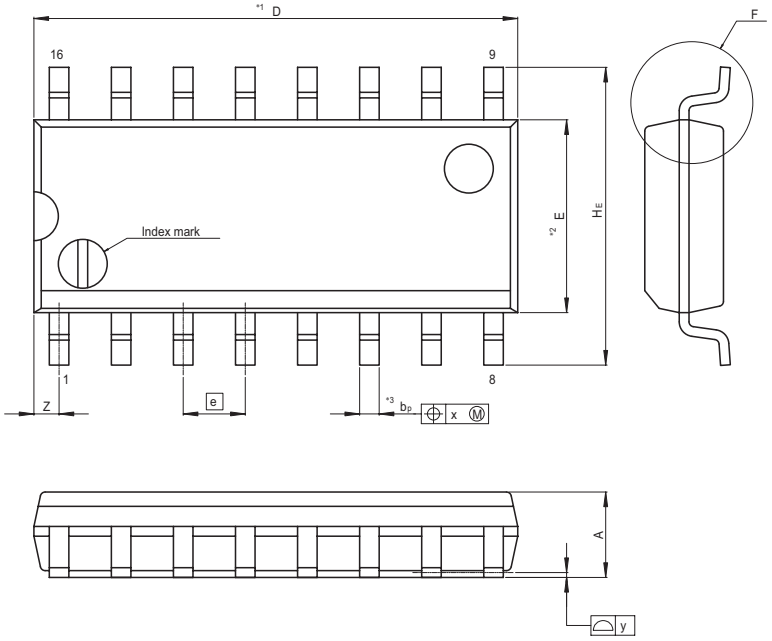
Terminal cross section
(Ni/Pd/Au plating)



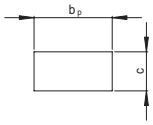
Detail F

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	——	10.06	10.5
E	——	5.50	——
A 2	——	——	——
A 1	0.00	0.10	0.20
A	——	——	2.20
b p	0.34	0.40	0.46
b 1	——	——	——
c	0.15	0.20	0.25
c 1	——	——	——
θ	0°	——	8°
H E	7.50	7.80	8.00
e	——	1.27	——
x	——	——	0.12
y	——	——	0.15
Z	——	——	0.80
L	0.50	0.70	0.90
L 1	——	1.15	——

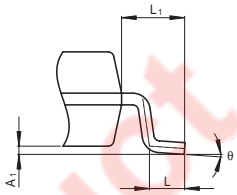
JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP16-3.95x9.9-1.27	PRSP0016DG-A	FP-16DNV	0.15g



NOTE)
1. DIMENSIONS"*1 (Nom)"AND"*2" DO NOT INCLUDE MOLD FLASH.
2. DIMENSION"*3"DOES NOT INCLUDE TRIM OFFSET.



Terminal cross section
(Ni/Pd/Au plating)



Detail F

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	——	9.90	10.30
E	——	3.95	——
A_2	——	——	——
A_1	0.10	0.14	0.25
A	——	——	1.75
b_p	0.34	0.40	0.46
b_1	——	——	——
c	0.15	0.20	0.25
c_1	——	——	——
θ	0°	——	8°
H_E	5.80	6.10	6.20
e	——	1.27	——
x	——	——	0.25
y	——	——	0.15
Z	——	——	0.635
L	0.40	0.60	1.27
L_1	——	1.08	——

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