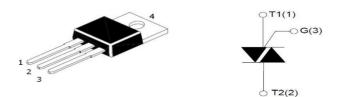




16A TRIACS



BTA16-600/800/1200 TO-220 (Ins) Plastic Package

BTB16-600/800/1200 TO-220 (Non-Ins) Plastic Package

BTA16 series triacs, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, 3 quadrant products expecially recommended for use on inductive load.

ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	VALUE	UNIT
Storage junction temperature range		T _{stg}	-40 to 150	°C
Operating junction temperature range		Tj	-40 to 125	°C
Repetitive peak of	f-state voltage (T _j =25°C)	V _{DRM}	600/800/1200	V
Repetitive peak re	verse voltage (T _j =25°C)	V _{RRM}	600/800/1200	V
Non repetitive surg	ge peak Off-state voltage	V _{DSM}	V _{DRM} +100	V
Non repetitive peak reverse voltage		V _{RSM}	V _{RRM} +100	V
RMS on-state current	TO-220 (Ins) (T _c =86°C) TO-220 (Non-Ins) (T _c =107°C)	I _{T(RMS)}	16	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)		I _{TSM}	160	А
I²t value for fusing	g (t _p =10ms)	l ² t	128	A ² s
Critical rate of rise of on-state current $(I_{G} = 2 \times I_{GT})$		dl/dt	50	A/µs
Peak gate current		I _{GM}	4	А
Average gate power dissipation		P _{G(AV)}	1	W
Peak gate power		P _{GM}	5	W



ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}c$ unless otherwise specified)

3 Quadrants (V _{DRM} /V _{RRM} : 600/800V)

PARAMETER	TEST CONDITIONS	SYMBOL	QUADRANT		VAL	JES		UNITS
				BW	CW	SW	TW	
Gate Trigger Current	V 42V B 220	Ι _{GT}	- -	<50	<35	<10	<5	mA
Gate Trigger Voltage	$V_{D} = 12V R_{L} = 33\Omega$	V _{GT}	- -		<1.3			V
Off-State Gate Voltage	$V_{D} = V_{DRM} T_{j} = 125 \degree C$ $R_{L} = 3.3 K\Omega$	V_{GD}	- -	>0.2			V	
		IL -	-	<70	<50	<30	<15	• mA
Latching Current	I _G =1.2I _{GT}		II	<80	<60	<40	<20	
Holding Current	I _T =100mA	I _H		<60	<40	<25	<15	mA
Critical Rate of Rise of Off-State Voltage	V _D =2/3V _{DRM} Gate Open T _j =125°C	dV/dt		>1000	>500	>200	>100	V/µs

4 Quadrant (V _{DRM}/V _{RRM}: 600/800V)

PARAMETER	TEST CONDITIONS	SYMBOL	QUADRANT	VALU	JES	UNITS
				В	C	
Gate Trigger Current		І _{бт}	- - V	<50 <70	<25 <50	mA
Gate Trigger Voltage	$V_{\rm D} = 12 V R_{\rm L} = 33 \Omega$	V _{GT}	ALL	<1.5		V
Off-State Gate Voltage	$V_{D} = V_{DRM} T_{j} = 125^{\circ}C \qquad R_{L}$ $= 3.3K\Omega$	V _{GD}	ALL	>0.2		V
Latching Current	I _G =1.2I _{GT}	IL.	- - V 	<70 <50 <100 <80		mA
Holding Current	I _T =100mA	I _H		<60	<40	mA
Critical Rate of Rise of Off-State Voltage	$V_D=2/3V_{DRM}$ Gate Open T _j =125°C	dV/dt		>500	>200	V/µs





3 Quadrants (V DRM/V RRM: 1200V)

PARAMETER	TEST CONDITIONS	SYMBOL	QUADRANT	VALUES	UNITS
Gate Trigger Current	V 12V P 220	Ι _{GT}	- -	<50	mA
Gate Trigger Voltage	$V_{D} = 12V R_{L} = 33\Omega$	V _{GT}	- -	<1.5	V
Off-State Gate Voltage	$V_{D} = V_{DRM} T_{j} = 125 \degree C$ $R_{L} = 3.3 K\Omega$	V_{GD}	- -	>0.2	V
Latching Current	I _G =1.2I _{GT}	١L	- 	<70 <90	mA
Holding Current	I _T =100mA	Iн		<60	mA
Critical Rate of Rise of Off-State Voltage	$V_{D}=2/3V_{DRM}$ Gate Open T _j =125°C	dV/dt		>1500	V/µs

STATIC CHARACTERISTICS

PARAMETER	TEST CONDITIONS		SYMBOL	VALUE (MAX)			UNITS
FARAMETER	TEST CONDITIONS	CONDITIONS		-600V	-800V	-1200V	
On-State Voltage	I _{TM} =22.5A t _p =380μs	T _j =25°C	V _{TM}	1.5			V
Off-State Leakage		T _j =25°C	I _{DRM}	5	5	10	μA
$V_{\rm D} = V_{\rm DRM} , V_{\rm R} = V_{\rm RRM}$	T _j =125°C	I _{RRM}	2	2	1	mA	

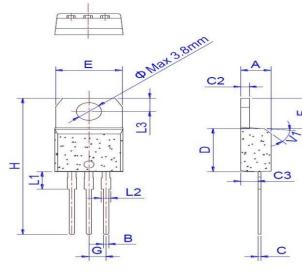
THERMAL RESISTANCES

PARAMETER		SYMBOL	VALUE (MAX)	UNITS
Maximum Thermal	TO-220 (Ins)	D	2.1	°C/W
Resistance	TO-220 (Non-Ins)	K _{th(j-c)}	1.2	C/ W

ORDERING INFORMATION

BTA12-XY	
BTB12-XY	
$X = 600$: VDRM/VRRM ≥ 600	$Y = BW$: $I_{GT1-3} \le 50mA$
= 800: VDRM/VRRM ≥ 800	= CW: $I_{GT1-3} \leq 35mA$
= 1200: VDRM/VRRM ≥ 1200	= SW: I _{GT1-3} ≤ 10mA
	= TW: I _{GT1-3} ≤ 5mA
	= B: I _{GT1-3} ≤50mA I _{GT4} ≤70mA
	= C: $I_{GT1-3} \leq 25mA$ $I_{GT4} \leq 50mA$

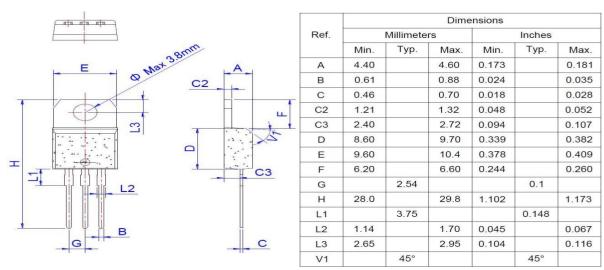




TO-220 (Ins) PACKAGE OUTLINE AND DIMENSIONS

	Dimensions							
Ref.		Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Typ.	Max.		
A	4.40		4.60	0.173		0.181		
в	0.61		0.88	0.024		0.035		
С	0.46		0.70	0.018		0.028		
C2	1.21		1.32	0.048		0.052		
C3	2.40		2.72	0.094		0.107		
D	8.60		9.70	0.339		0.382		
E	9.80		10.4	0.386		0.409		
F	6.55		6.95	0.258		0.274		
G		2.54			0.1			
Н	28.0		29.8	1.102		1.173		
L1		3.75			0.148			
L2	1.14		1.70	0.045		0.067		
L3	2.65		2.95	0.104		0.116		
V1		45°			45°			

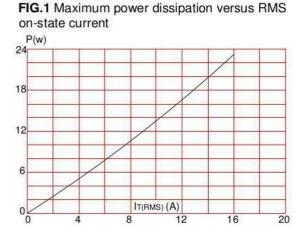
TO-220 (Non-Ins) PACKAGE OUTLINE AND DIMENSIONS

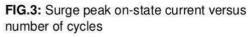






CHARACTERISTIC CURVES





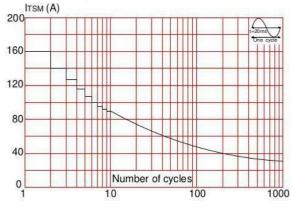


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of $l^{2}t$ (dl/dt < 50A/µs)

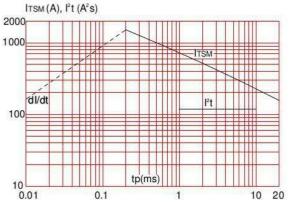
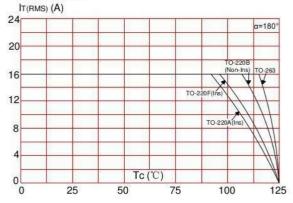
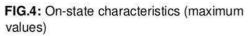


FIG.2: RMS on-state current versus case temperature





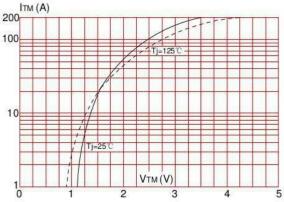
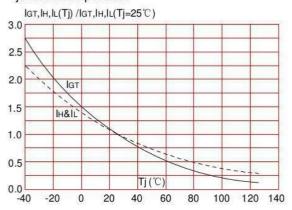


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature







Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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