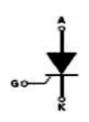




2.5 Amp SCR





C106M C106D

TO-126 Leaded Plastic Package RoHS compliant

TO-126

FEATURES:

- 1. High Blocking Voltage
- 2. Low On-State Voltage and high I_{TSM}
- 3. RoHS Compliant

APPLICATIONS

Applications in all Models of Control like Phase control, Heating Control, Voltage Regulation Circuits etc.

DADAMETED	SYMBOL	TEST CONDITION	VALUE			
PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT	
Repetitive Peak Off-State Voltage	V	C106M		600	V	
Repetitive Feak OII-State voltage	V _{DRM}	C106D	400		V	
Repetitive Peak Reverse Voltage	V	C106M				
Repetitive Fear Reverse voltage	V _{RRM}	C106D		400	V	
Average On-State Current	I _{T(AV)}	Half Sine wave,		2.5	A	
		Tamb ≤ 109°C				
On-State RMS Current	I _{T(RMS)}	All Conduction Angles		4	А	
Non-Repetitive Surge Peak On-state Current	I _{TSM}	Full Sine Wave, TJ=25°C,		40	А	
	l ² t	t=10ms		6	A ² s	
Repetitive Rate of Rise of On-State	di/dt	I _{TM} =20A, I _G =0.2A,		50	A/µs	
Peak Gate Current	I _{GM}			0.5	А	
Peak Gate Power	P _{GM}			1	W	
Average Gate Power	P _{G(AV)}	over any 20ms period		0.2	W	
Storage Temperature Range	T _{STG}		-40	150	°C	
Operating Junction Temperature	TJ			125	°C	

THERMAL RESISTANCE

DADAMETED	SYMBOL	VALUE			
PARAMETER	STNIDUL	MIN	TYP	MAX	UNIT
Junction to Case (AC)	R _{th(J-C)}			3.0	K/W
Junction to Ambient	R _{th(J-A)}		75		K/W





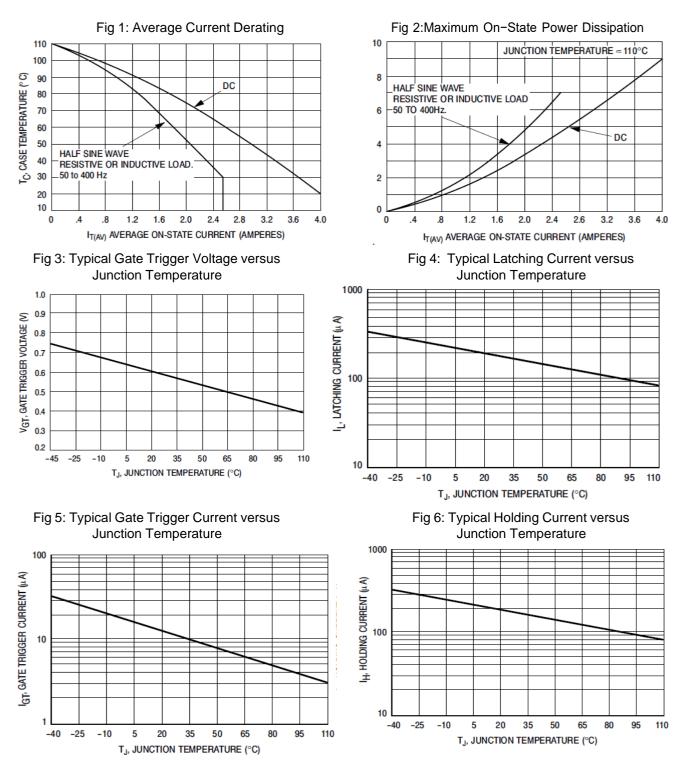
ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER		VALUE			UNIT	
PARAMETER	SYMBOL TEST CONDITION		MIN	TYP	MAX	
Peak Repetitive Forward Blocking Current	I _{DRM}	$V_{DM}=V_{DRM(MAX),}T_{J}=125^{\circ}C$		0.1	0.5	mA
Peak Repetitive Reverse Blocking Current	I _{RRM}	$V_{RM} = V_{RRM(MAX),} T_J = 125^{\circ}C$		0.1	0.5	mA
Peak On-State Voltage	V _{TM}	I _{TM} =4A		1.3	2.2	V
Gate Trigger Current	I _{GT}	V _{DM} =12V, I _T =0.1A		50	200	μA
Gate Trigger Voltage	V _{GT}	V _{DM} =12V, I _T =0.1A		0.4	1.5	V
Holding Current	Iн	V _{DM} =12V, I _{GT} =0.1A		0.3	6	mA
Latching Current	١L	V _{DM} =12V, I _{GT} =0.1A		0.4	10	mA
Rise of Off-State Voltage	dV/dt	Vdm=67%Vdrm(max), Tj=125°C	50	100		V/µs
Gate Controlled Turn-On Time	tgt	I _{TM} =40A, V _{DM} =V _{DRM(MAX),} I _G =0.1A, dI _G /dt=5A/µs		2		μs





TYPICAL CHARACTERISTICS CURVES



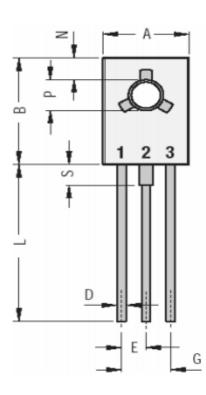
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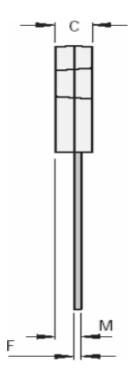




PACKAGE DETAILS

TO-126 Leaded Plastic Package





DIM.	MIN.	TYP.	MAX.
A	7.4		7.8
В	10.5		10.8
С	2.5		2.7
D	0.64		0.88
Е		2.25	
F	0.39		0.63
G		4.5	
L		15.7	
М		1.27	
Ν		3.75	
Р	2.9		3.2
S		2.5	

All dimensions in mm

PIN CONFIGURATION

- 1: CATHODE
- 2: ANODE
- 3: GATE





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.

